

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu SUZUKI, et al.
Serial No: 10/722,597
Filed: November 28, 2003
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEREOF
Group: 2822
Examiner: Monica LEWIS

PETITION FOR AN EXTENSION OF TIME**Mail Stop EOT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 4, 2007

Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing of a response within the second month subsequent to expiration of the shortened statutory period set in the Final Rejection of April 2, 2007.

A Credit Card Payment Form in the amount of \$450.00 to cover the fee required for the requested extension of time is included herewith.

It is respectfully requested that any shortage in the fee be charged to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (520.43306X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

/Larry N. Anagnos/
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LNA:dlh